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APPLICATION NO.	F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/683,486	9/683,486 01/07/2002		Andres Bryant	BUR920010086 2808	
30607	7590	04/23/2003			
		EN & WATTS LL	EXAMINER		
MESA, AZ		TY DRIVE, #101		GEBREMARIA	M, SAMUEL A
				ART UNIT	PAPER NUMBER
				2811	
			DATE MAILED: 04/23/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

	•			1.0
		Application No.	Applicant(s)	X,
İ	Office Action Survey	09/683,486	BRYANT ET AL.	
	Office Action Summary	Examiner	Art Unit	
	•	Samuel A Gebremariam	2811	
Peri d fo	The MAILING DATE of this communication ap or Reply	pears on the cover sheet with the	ne correspondence address	
THE I - Externanter - If the - If NO - Failu - Any r	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1. SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a replay period for reply is specified above, the maximum statutory period are to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing adaptent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a reply be a reply by within the statutory minimum of thirty (30) will apply and will expire SIX (6) MONTHS the cause the application to become ABAND	the timely filed days will be considered timely. from the mailing date of this communic	cation.
1)[Responsive to communication(s) filed on 21	February 2003 .		
2a) <u></u>		nis action is non-final.		
3)□ Dispositi	Since this application is in condition for allow closed in accordance with the practice under on of Claims	ance except for formal matters	, prosecution as to the mer 1, 453 O.G. 213.	its is
4)🖂	Claim(s) 12-25 is/are pending in the application	on.		
	4a) Of the above claim(s) is/are withdra	wn from consideration.		
	Claim(s) 21-25 is/are allowed.			
6)🖂	Claim(s) 12-16 and 18-20 is/are rejected.			
7)🖂	Claim(s) 17 and 19 is/are objected to.			
	Claim(s) are subject to restriction and/o	or election requirement.		
	on Papers	,	•	
9) 🗌 1	The specification is objected to by the Examine	er.		
10)[] 7	Γhe drawing(s) filed on is/are: a)□ acceμ	pted or b)⊡ objected to by the E	xaminer.	
	Applicant may not request that any objection to the	e drawing(s) be held in abeyance.	See 37 CFR 1.85(a).	
11) 🗌 T	The proposed drawing correction filed on	_is: a)∏ approved b)∏ disap _l	proved by the Examiner.	
	If approved, corrected drawings are required in rep	· · ·		
12)∐ T	The oath or declaration is objected to by the Ex	aminer.		
Priority u	nder 35 U.S.C. §§ 119 and 120			
13) 🗌 🛚	Acknowledgment is made of a claim for foreigr	n priority under 35 U.S.C. § 119	9(a)-(d) or (f).	
a)[☐All b)☐ Some * c)☐ None of:			
	1. Certified copies of the priority documents	s have been received.		
	2. Certified copies of the priority documents	s have been received in Applic	ation No	
	 Copies of the certified copies of the prior application from the International Bure the attached detailed Office action for a list 	rity documents have been rece reau (PCT Rule 17.2(a)).	ived in this National Stage	
	cknowledgment is made of a claim for domestic			eation)
a)	☐ The translation of the foreign language pro cknowledgment is made of a claim for domesti	visional application has been re	eceived.	ation).
Attachment(- priority and or 0.0.0. 33 1.	20 anu/01 121.	
1) Notice 2) Notice	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO-1449) Paper No(s) 2	5) Notice of Informa	ary (PTO-413) Paper No(s) al Patent Application (PTO-152)	_•
S. Patent and Trac TO-326 (Rev.		tion Summary	Part of Paper I	No. 5

Application/Control Number: 09/683,486

Art Unit: 2811

DETAILED ACTION

Election/Restrictions

1. Applicant's election without traverse of group II, claims 12-25 drawn to a semiconductor device in Paper No. 4 is acknowledged.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 12, 14, 15, 18 and 20 are rejected under 35 U.S.C. 102(b) as being anticipated by Bryant et al., US patent No. 6,159,807.

Regarding claim 12, Bryant teaches (figs. 1, 3 and 4) a transistor comprising: a) a transistor body (42) formed on a substrate (12), the transistor body having a first vertical edge (the edge near the left trench (50)) and a second vertical edge (the edge near the right trench (50)), b) a gate structure (56) adjacent the transistor body first vertical edge (portion of the gate on the left hand side the transistor); c) a body contact structure (16) adjacent the transistor body second vertical edge and aligned with the gate, d) a bridge (72) over the body (42), the gate (56), and the body contact (16), electrically connecting the gate and the body contact; and e) source (18) and drain (20) regions in the body on opposite ends of the body.

Application/Control Number: 09/683,486

Art Unit: 2811

Regarding claim 14, Bryant teaches (fig. 3a) the entire claimed structure of claim 12 above including the gate structure comprises n-type material (col. 5, lines 36-38) and wherein the body contact structure comprises p-type (col. 5, lines 58-63) material.

Regarding claim 15, Bryant teaches (fig. 3a, col. 3, lines 58-63) the entire claimed structure of claim 12 above including the transistor body (42) comprises a portion of silicon of the silicon-on-insulator layer.

Regarding claim 18, Bryant teaches (fig. 1) the entire claimed structure of claim 12 above including the transistor body (42) comprises source (20) and drain (18) implants into the transistor body, the implants aligned with the edges of the body contact.

Regarding claim 20, Bryant teaches (fig. 3a) the entire claimed structure of claim 12 above including the transistor body first edge (the edge near the left trench (50)) is opposite the transistor body second edge (the edge near the right trench (50)) and wherein the transistor body first edge and transistor body second edge are substantially perpendicular to a top surface of the substrate (12).

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 13 and 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bryant.

Application/Control Number: 09/683,486

Art Unit: 2811

Regarding claim 13, Bryant teaches substantially the entire claimed structure of claim 12 above except explicitly stating that the gate structure comprises p-type material and wherein the body contact structure comprises n-type material.

Bryant teaches gate structure comprising n-type material (col. 5, lines 36-38) and wherein the body contact structure comprising p-type (col. 5, lines 58-63) material.

Furthermore parameters such as doping type and concentration in the art of semiconductor manufacturing process are subject to routine experimentation and optimization to achieve the desired device characteristics during fabrication.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to adjust the doping type as claimed in order to form a CMOS device.

Regarding claim 16, Bryant teaches substantially the entire claimed structure of claim 12 above except explicitly stating the thickness of the transistor body between the gate structure and the body contact structure is less than one-third of the length of the gate structure.

Parameters such as thickness and width in the art of semiconductor manufacturing process are subject to routine experimentation and optimization to achieve the desired device characteristics during fabrication.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to adjust the thickness of the transistor body as claimed in order to form a CMOS device.

Allowable Subj ct Matt r

6. Claims 17 and 19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Allowance

7. Claims 21-25 are allowed.

Reason for allowance

8. The following is a statement of reasons for the indication of allowable subject matter: prior art of record does not anticipate or render obvious the limitation of "a body contact native oxide layer formed on the transistor body second edge; a body contact structure formed on the body contact native oxide layer aligned to the gate structure and adjacent to the transistor body second edge" for a transistor body.

Conclusion

9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Reference A is cited as being related to transistor structure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Samuel Admassu Gebremariam whose telephone number is 703 305 1913. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on 703-308-2772. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7722 for After Final communications.

Art Unit: 2811

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Samuel Admassu Gebremariam April 18, 2003

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